

16424 U.S. PTO
10/650601
08/28/03

Sir:

Transmitted herewith for filing is the Patent Application of:

Inventor: **SHI-CHI LIN**

For: **METHOD OF MANUFACTURING DIELECTRIC ISOLATED SILICON STRUCTURE**

Enclosed are:

- ☒ **3** sheets of drawing(s) - formal.
- ☒ An assignment of the invention to **Taiwan Semiconductor Manufacturing Co.**
- ☐ An associate power of attorney ☐ Applicant claims small entity status
- ☐ Request & Certification under 35 USC 122(b)(2)(b)(i)

The filing fee has been calculated as shown below:

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TOTAL CLAIMS	20 -20=	0	x 18 =	\$ 0.
INDEP CLAIMS	3 -3=	0	x 84 =	\$ 0.
SUB TOTAL				\$ 750.
ASSIGNMENT				\$40.
TOTAL				\$ 790.

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- ☒ Any additional filing fees required under 37 CFR §1.16.
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Respectfully submitted,

STEPHEN B. ACKERMAN, REG. NO. 37,761

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